

M. Tech in VLSI Design, with 5+ Years of experience teaching EC/VLSI Design courses to Engineering Students**EXPERIENCE**

- Associate Professor – Jan, 2018 – Dec, 2023 @ GyanSagar College of Engineering, Sagar
- Assistant Professor – Aug, 2017 – Jan 2018 @ Adina Institute of Science and Technology, Sagar
- Freelance Tutor at Transtutor

KNOWLEDGE OF VLSI TOOLS AND TECHNOLOGIES

- Verilog
- Xilinx
- ExpressPCB
- Verification and simulation
- ModelSim
- FPGA

SUBJECTS TAUGHT TO STUDENTS

- VLSI
- Digital Electronics
- Analog Electronics
- Microprocessors and Microcontrollers
- Digital Communication
- Electronics Devices and Circuits
- Microwave

KNOWLEDGE OF SOFTWARE / IT TECHNOLOGIES

- Java / OOPS Concepts
- Selenium, TestNG, Cucumber Framework

PAPERS PUBLISHED

- [Ref 1] FPGA Based Circuit Design for Power Loss Estimation and Mitigation
- [Ref 2] Review Paper on FPGA and Its Applications

RESEARCH WORK (THESIS)

- [Ref 3] M. Tech. (VLSI) – Topic of Research: Implementation of FPGA through self testing mechanism of UART
Software Tool – MATLAB

EDUCATION

- **M. Tech. (VLSI)** – 8.2 / 10 CGPA, 2017 – 2019, Rajiv Gandhi Proudhyogiki Vishwavidyalaya (RGPV)
- **B. E. (ECE)** – 8.5 / 10 CGPA, 2012 – 2016, Rajiv Gandhi Proudhyogiki Vishwavidyalaya (RGPV)
- **12th Class** – 83%, 2012, MP Board

[Ref 1] -

https://www.linkedin.com/posts/aakriti-jain-a55422112_ref-1-activity-7117173101762625536-kwm1?utm_source=share&utm_medium=member_desktop

[Ref 2] -

https://www.linkedin.com/posts/aakriti-jain-a55422112_ref-2-activity-7117195142628843522-X8eP?utm_source=share&utm_medium=member_desktop

[Ref 3] -

https://www.linkedin.com/posts/aakriti-jain-a55422112_ref-3thesis-topic-implementation-of-fpga-activity-7117199345581498369-NHO?utm_source=share&utm_medium=member_desktop